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Stemless InSb nanowire networks and nanoflakes grown on InP

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Abstract

Among the experimental realization of fault-tolerant topological circuits are interconnecting nanowires with minimal disorder. Out-of-plane indium antimonide (InSb) nanowire networks formed by merging are potential candidates. Yet, their growth requires a foreign material stem usually made of InP–InAs. This stem imposes limitations, which include restricting the size of the nanowire network, inducing disorder through grain boundaries and impurity incorporation. Here, we omit the stem allowing for the growth of stemless InSb nanowire networks on an InP substrate. To enable the growth without the stem, we show that a preconditioning step using arsine (AsH₃) is required before InSb growth. High-yield of stemless nanowire growth is achieved by patterning the substrate with a selective-area mask with nanohole cavities, containing restricted gold droplets from which nanowires originate. Interestingly, these nanowires are bent, posing challenges for the synthesis of interconnecting nanowire networks due to merging failure. We attribute this bending to the non-homogeneous incorporation of arsenic impurities in the InSb nanowires and the interposed lattice-mismatch. By tuning the growth parameters, we can mitigate the bending, yielding large and single crystalline InSb nanowire networks and nanoflakes. The improved size and crystal quality of these nanostructures broaden the potential of this technique for fabricating advanced quantum devices.

Supplementary material for this article is available [online](#)

Keywords: nanowires, metal organic vapor phase epitaxy, InSb, bending, nanoflakes

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1. Introduction

Indium antimonide (InSb) is a semiconductor with ideal properties for quantum applications. Its high mobility, strong spin-orbit coupling, and Landé g -factor make it suitable for studying quantum phenomena in semiconductor–superconductor device hybrids based on nanowires [1–5]. These device hybrids have gained attention for their potential to host Majorana-zero-modes (MZMs), vital for enabling fault-tolerant topological quantum computation [6–8]. However, detecting MZMs experimentally has proven to be challenging due to material disorder [9–13], thus their existence remains an open question.

One method to demonstrate the existence of MZMs is by showcasing their non-abelian statistics through controlled braiding, requiring a complex network of interconnecting nanowires with a selectively deposited superconductor [14–16]. Interconnecting out-of-plane InSb nanowire networks, grown on indium phosphide (InP) or indium arsenide (InAs) intermediary stems via the gold (Au) catalyzed vapor–liquid–solid (VLS) method, are promising infrastructures for these experiments [17–19], with reports of ballistic transport in this system [20, 21]. Nanowire interconnection between out-of-plane nanowires was realized by changing the growth direction during stem growth [17, 18] or by growing non-parallel stems on the inclined {111} facets of a (100) InP or InAs substrate [19, 21]. Moreover, the use of stems facilitates InSb nanowire growth as the direct nucleation of InSb nanowires on the substrate is challenging due to the surfactant effect of antimony (Sb) [22, 23], which causes the Au droplet to be propelled on the surface rather than promoting vertical nanowire growth [24]. Nonetheless, stems give rise to problems for MZMs experiments. The mixed-crystal-phase nature of the stems induces disorder in interconnecting InSb nanowires causing the formation of grain boundaries [17, 18, 25] (see figure S1(a) in the supplementary information) or lead to arsenic (As) or phosphorous (P) impurity incorporation [24, 26]. Moreover, they restrict the overall extension of the semiconductor–superconductor device hybrid since stems evaporate during InSb growth [24, 27, 28], thereby limiting the InSb nanowire growth time and thus its length to approximately $3.5 \mu\text{m}$ (figure S1(b)). Larger nanowire networks are necessary to accommodate gate connections, multiple superconducting islands [29] and reduce wavefunction overlap among different MZMs [30] (figure S1(c)).

In this study, we present a method for achieving stemless InSb nanowires on InP adopting a preconditioning step with arsine (AsH_3) before InSb growth and by effectively confining the Au droplets using a selective-area mask fabricated on the growth substrate. However, nanowires grown using this method exhibit bending, which could hinder nanowire interconnection when growing out-of-plane networks. To address this issue, we conducted a comprehensive investigation including statistical analysis, material characterization and modeling to identify the causes of bending and explore mitigation strategies. We explain the origin of the bending, rooted in the prior use of AsH_3 in the growth chamber, and resulting from the non-homogeneous incorporation of As impurities in

the nanowires following an asymmetrical core–shell scheme, causing lattice mismatch. Armed with this improved understanding of bending, we successfully mitigated it by tuning the growth parameters, achieving single-crystalline stemless nanowire networks and nanoflakes, surpassing their stemmed counterparts in length, reaching sizes of tens of microns.

2. Methods

InP(111)B (for vertical nanowire growth) and InP(100) substrates with etched V-grooves (for interconnecting nanowire growth, as reported at [25]) have been prepared for nanowire growth. The fabrication of the amorphous selective-area silicon nitride (SiN_x) mask with nanoholes on both these substrates starts by depositing a 20 nm thin layer of SiN_x on the substrate using plasma-enhanced physical vapor deposition. A positive resist is spun on the substrate and circular nanoholes with varying pitch are then patterned using electron-beam lithography. After resist development, two methods for nanohole fabrication within the SiN_x mask are tested: one set of samples undergoes a dry etching process utilizing a reactive-ion etching tool, while another set undergoes wet etching using a buffered hydrofluoric acid solution. A 8 nm thin layer of Au is deposited on both sets of samples by electron-beam physical vapor deposition, followed by resist lift-off, leaving Au only in the nanoholes. Dry-etched nanoholes have parallel sidewalls, whose pattern is established by the lithography design, and contain Au droplets with a similar diameter (see figures S2(a), (c), (e)). On the other hand, wet-etched nanoholes have inclined sidewalls and are usually larger compared to the intended size of the lithography design due to the isotropic nature of the etching, resulting in Au droplets with a significantly smaller diameter than the nanoholes (see figures S2(b), (d), (f)). The detailed fabrication recipe of the substrate can be found in section SI.1 of the supplementary information.

The InSb nanowire growth is performed in a horizontal Aixtron MOVPE reactor, using molecular hydrogen as carrier gas for the precursors, which are trimethylantimony (TMSb) and trimethylindium (TMIn). The InSb growth is preceded by a heating-up step using either TMSb, or an alternative group-V precursor, namely phosphine (PH_3) or AsH_3 . The growth scheme used to test the nanowire growth using the PH_3 or AsH_3 heat-up steps is sketched in figure 1(b). In case of PH_3 or AsH_3 heat-up, InSb growth is started only for 30 s (‘nucleation’ step) and followed by a step in which the reactor is flushed with TMSb for 2 min to remove any PH_3 or AsH_3 residuals. Subsequently, InSb growth is performed for 90 min. The growth with a TMSb heat up step follows a similar growth scheme developed in a previous work on the homoepitaxial growth of InSb nanowires [24]. The growth runs using different group-V species during heating-up are performed in separate reactors to avoid cross-contamination, and this is achieved by replacing the quartz reactor walls and substrate susceptor between different runs. The samples are kept under constant rotation during the whole duration of the growth scheme. Additional growth details can be found in section SI.1 of the supplementary information.

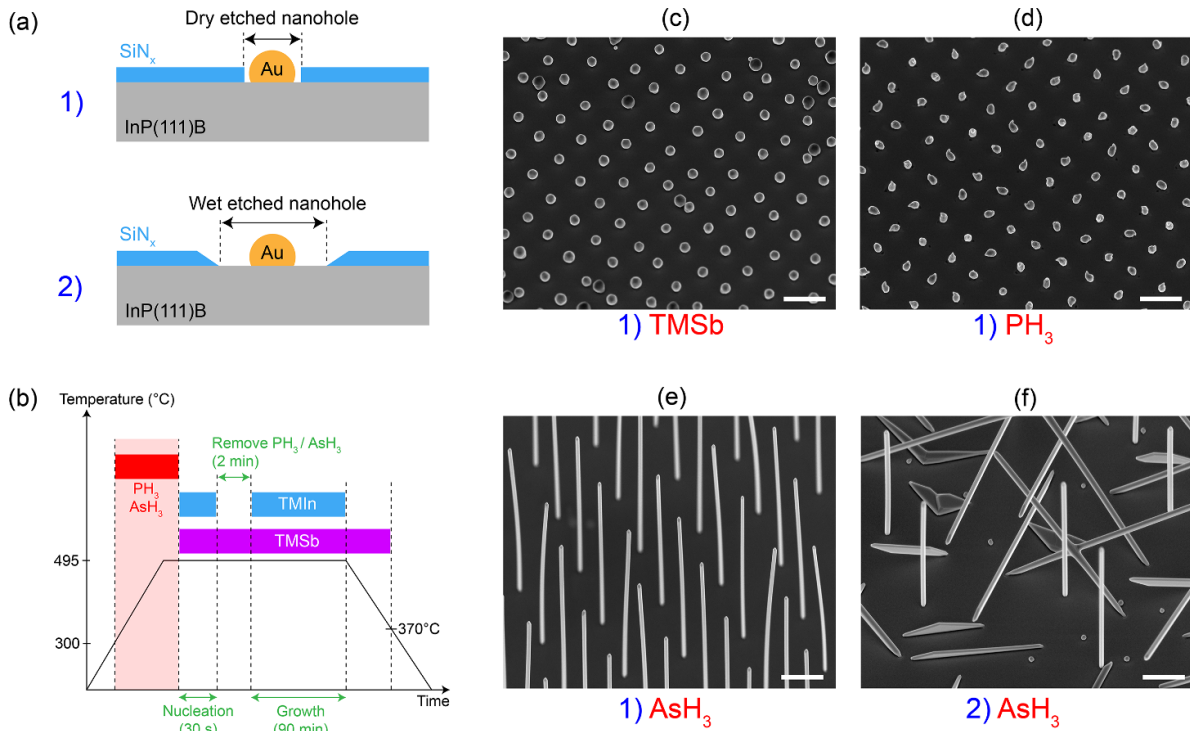


Figure 1. (a) Sketches of the substrates for nanowire growth. Au droplets are confined within SiN_x nanoholes defined by lithography and etched with dry or wet etching, which yield two different cavity shapes. (b) Sketch of the MOVPE growth scheme used to test the stemless InSb nanowire growth. (c)–(f) Representative side view SEM images of the growth outcomes using the substrates shown in (a) and different group-V species during heat-up. (c) Heat-up under TMSb and dry-etched nanoholes (growth scheme of [24]). (d), (e) Dry-etched nanoholes and heat-up under PH_3 and AsH_3 , respectively, using the growth scheme shown in (b). (f) Wet-etched nanoholes and heat-up under AsH_3 , using the growth scheme shown in (b). Scale bars are $1 \mu\text{m}$. Pictures (c)–(f) were taken with the SEM stage tilted by 30° .

3. Results and discussion

The goal of this work is to develop advanced self-shadowing nanowire network structures for novel semiconductor–superconductor quantum devices based on InSb nanowires. To achieve this, an InP(100) platform with etched V-grooves, as developed in previous studies [19, 25, 31], will be utilized for growing such nanostructures. In these previous works, tilted InSb nanowires have been grown using InAs/InP stem nanowires on the $\{111\}$ B-sloped facets. In this work we want to develop a process to grow InSb nanowires on InP substrates without using a stem to be able to grow much longer InSb nanowires [24]. We therefore start this work with developing the growth on planar InP(111)B substrates and in the last part of the manuscript we discuss growth in the V-grooves, consisting of $\{111\}$ B facets in a (100) wafer. For the growth of InSb nanowires on InP(111)B we first study the effect of the shape of the nanohole cavity in the SiN_x mask and the preconditioning step on the InSb nanowire growth. The two different nanohole cavities are illustrated in figure 1(a), and are realized by two different fabrication processes (see methods section and section SI.1 of the supplementary information) and basically differ in the ratio of the Au-island size with respect to the nanohole opening size. This ratio is a very important parameter that influences the nucleation and growth of the nanowires, as shown in a previous study for the growth on InSb(111) substrates [24]. To study the effect of preconditioning, the

samples have been exposed to TMSb, AsH_3 or PH_3 while the sample is heated up from 300°C to 495°C prior the growth, following the growth scheme of figure 1(b).

Figures 1(c)–(e) are representative scanning electron microscopy (SEM) images of the growth on samples with dry-etched nanoholes that underwent one of the three different preconditioning steps. Both the growth with TMSb and PH_3 heat-up steps show that InSb crystals have nucleated within each nanohole without leading to any vertical nanowire growth. On the other hand, the exposure to AsH_3 during heat-up leads to a high yield of long ($\sim 10 \mu\text{m}$) vertical InSb nanowires having a thickness ranging from 85 to 115 nm depending on the pitch (figures 1(e) and S3). Transmission-electron-microscopy (TEM) reveals that these nanowires display a defect-free zincblende crystal structure and are grown via the Au-catalyzed VLS mode (see figure S11). The sample with wet-etched nanoholes, grown in the same run, displays a low yield of vertical nanowire growth (figures 1(f) and S3). In this case, most of the Au droplets seem to slide laterally once exceeded the height of the nanohole cavity, forming nanostructures that in some cases merge with neighboring ones. The SiN_x mask effectively suppresses the formation of parasitic InSb islands on the substrate in every growth run. From this study it is evident that As-exposure during heating up is essential to initiate the growth of InSb nanowires, otherwise they do not nucleate. Furthermore, the small nanohole cavities result in a high yield of vertical InSb nanowires, which is

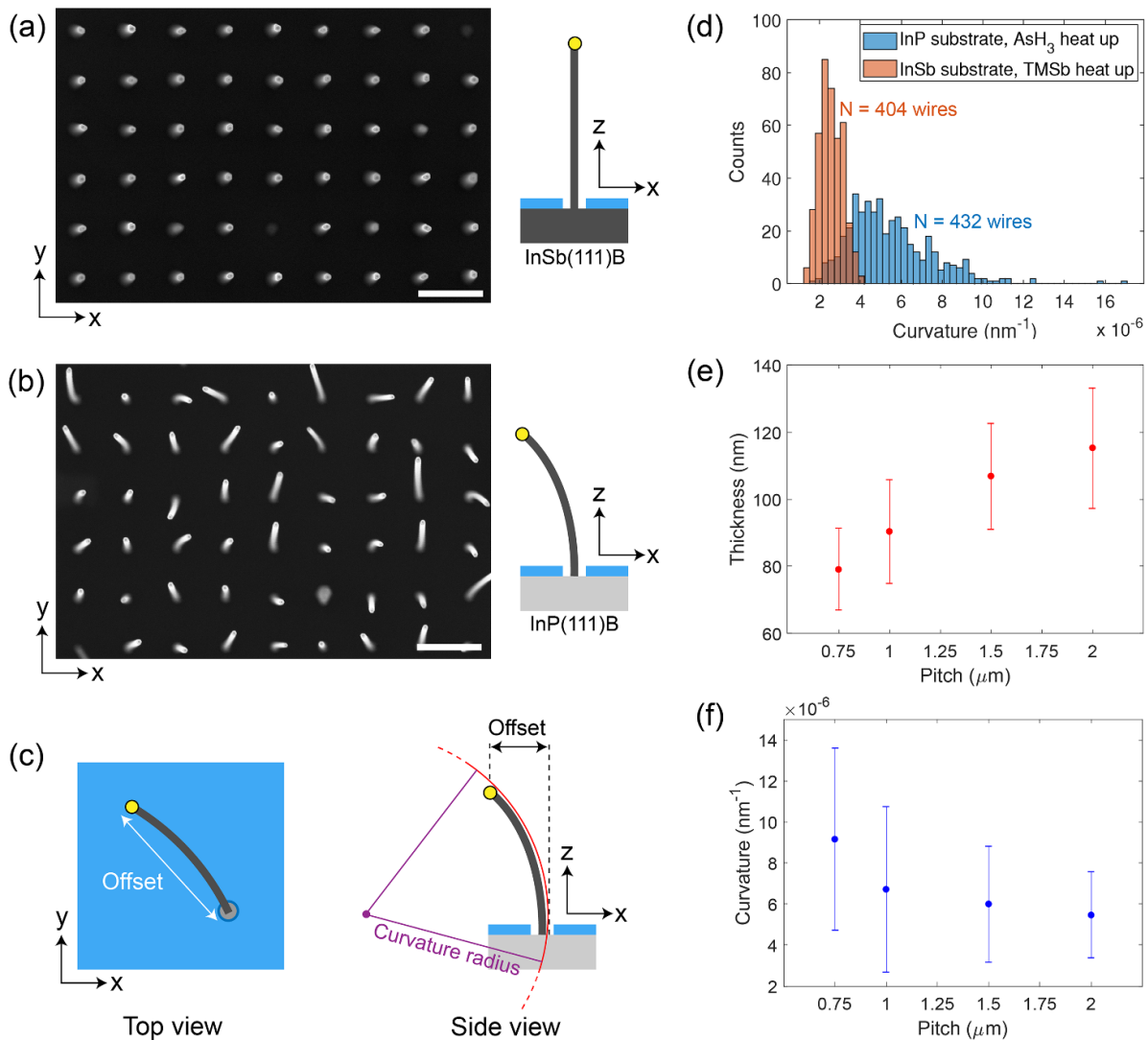


Figure 2. (a) Representative SEM top-view image of the stemless nanowires grown on InSb without employing AsH_3 and in an As-free reactor, and a sketch from the side view (b) representative SEM top-view image of the stemless nanowires grown on InP using AsH_3 during heat-up, and a sketch from the side view. Scale bars are $1 \mu\text{m}$. (c) Top-view and side-view sketches of a bent nanowire. The SEM image recognition software retrieves the ‘offset’ of each nanowire in an SEM top-view picture, which is used to estimate its curvature, i.e. the inverse of its curvature radius. (d) Comparison between the curvature distributions of a similar number (N) of stemless nanowires grown on InP using an AsH_3 heat-up (blue bins) and stemless nanowires grown on InSb using a TMSb heat-up (orange bins). (e), (f) Results of the statistical analysis of a sample of stemless InSb nanowires grown on InP from dry-etched nanoholes, displaying the (e) nanowire thickness and (f) nanowire curvature as a function of the pitch. Means and error bars of the curvature are calculated by fitting the curvature distributions of nanowire field with different pitch (see figure S9 of the supplementary information).

important for their use in the self-shadowing structures. We emphasize, however, that the InSb nanowires of which the growth has been initiated by AsH_3 preconditioning seem to be bent, especially the nanowires with the smaller diameters, which may affect the electronic properties [32, 33].

Therefore, we next investigate and quantify the bending of the InSb nanowires. As a reference sample, stemless InSb nanowires are grown on InSb(111)B substrates with a patterned SiN_x mask with dry-etched nanoholes. Growth is performed without using AsH_3 and in an As-free reactor (growth details can be found here [24]). The resulting nanowires appear

straight from the top-view SEM image (figure 2(a)), in contrast to the InSb wires grown on InP(111) by using AsH_3 (figure 2(b)). The curvature of the nanowires grown on these two substrates is determined from a statistically relevant number of nanowires (>400) using SEM image-recognition software (see section SI.2 of the supplementary information for details). The software analyzes both top-view and side-view SEM images of nanowire fields and extracts geometric information of each nanowire, e.g. nanowire thickness and length. To estimate the level of bending of nanowires, the software measures the ‘offset’ between base and tip of the nanowire when

viewed from top-view as illustrated in figure 2(c). Figure 2(d) compares the curvature distributions of InSb nanowires grown on InSb (orange) and InP (blue) substrates. Nanowires grown on InSb (in an As-free reactor) are all apparently ‘curved’ in the same direction, which can be explained by imperfect sample clamping prior SEM imaging (see figure S8 and section SI.2). The imperfect clamping procedure results in a consistent systematic error in the offset, leading to an apparent nanowire curvature, that we quantify at $2.7 \times 10^{-6} \text{ nm}^{-1}$. In contrast, nanowires grown on InP substrates with an AsH₃ preconditioning step display a significantly broader curvature distribution, with nanowires exhibiting curvatures even an order of magnitude greater than the systematic error (see figure S9). Therefore, the presence of this systematic error has a minimal impact on the determination of mean curvatures and spreads of nanowires grown using AsH₃. The study of the bending orientation of nanowires grown using AsH₃ reveals that they can be bent in every direction, although a preferential bending orientation seems to exist (see figure S9 and section SI.2). The underlying causes of this preferential orientation, which we do not further explore, remains unknown.

The influence of the pitch on the aspect ratio and bending is studied and the results of the analysis of a representative sample are shown in figures 2(e) and (f), highlighting that nanowire thickness and curvature exhibit opposite monotonic behavior, with the thickness increasing and curvature decreasing as a function of the pitch. The nanowire length is unaffected by the pitch (see figure S7). The thickness increase for larger pitch suggests a contribution of surface diffusion of growth units from the mask towards the side-facets of the nanowires. The opposite behavior of thickness and curvature suggests that thicker wires are stiffer and thus bend less.

To explain the bending of the nanowires, a first hypothesis is to attribute it to the 10.4% lattice mismatch between InSb and InP [1, 2], which typically induces defects when growing blanket InSb layers on mismatched substrates [34–37]. Nanowires are known to better accommodate lattice mismatch due to their small footprints [38, 39], but elastic strain may occur to prevent the formation of threading dislocations [40]. However, we have evidence that strain due to lattice mismatch is fully relaxed at the InP–InSb nanowire interface (see figures S12(a)–(c)). A second hypothesis for the bending behavior could be the use of As during heat up, which might lead to the known ‘carry-over’ effect [41–45]. Arsenic can stick to the internal parts of the growth chamber and outgas at high temperatures during all the subsequent growth steps, thus being unintentionally incorporated into the growing nanowires and form an InAs_xSb_{1-x} alloy. Considering that InSb and InAs have a lattice mismatch of about 7% [1, 2], the presence of As impurities with varying concentration within the InSb nanowires can lead to strain, and thus bending. This hypothesis is tested by employing the AsH₃ preconditioning on a InSb(111)B sample for nanowire growth. Interestingly, the resulting nanowires exhibit the same bending as those grown on an InP(111)B substrate (see figure S4), supporting the As carry-over hypothesis.

We perform cross-sectional scanning-transmission electron microscopy-energy dispersive x-ray spectroscopy (STEM-EDX) analysis to check the presence of As impurities in the nanowires grown using AsH₃ during preconditioning. Ten nanowires are transferred from the InP growth substrate to a bare wafer. Subsequently, cross-sectional TEM lamellas are created including all ten wires using a focused-ion beam; note that each wire is cut at a specific height from their base (see figure S13). The presence of As impurities is detected in all the nanowires under study, proving the occurrence of the As carry-over effect. Out of ten nanowires, eight nanowires (A1–C1 and A2–E2 in figure S13) possess two common characteristics, that can be visualized in the representative STEM-EDX cross-sectional cut shown in figure 3(a). First, the nanowires display a distorted hexagonal cross-section due to unequally long {110} side facets. Second, As is unevenly distributed within the nanowire cross-section forming an As-poor core surrounded by an As-rich shell with varying As fractions. The difference in As molar fraction between core and shell ranges from $x = 0.039$ to $x = 0.086$ and leads to an approximately 0.3% variation in lattice parameter of the InAs_xSb_{1-x} alloy [46]. The combination of these two characteristics results in an asymmetrical core–shell distribution of the lattice parameter, which is known to cause bending in nanowires [47–63]. We note that the As-poor core area is similar among the eight nanowires, while the thickness of the As-rich shell varies and seems to be connected to the pitch of the field from which the nanowires were transferred. We interpret the As-poor core area as the volume realized by the vertical Au-catalyzed VLS-growth and the As-rich outer shell as the radial, vapor-solid (VS) grown part. Possibly, these two growth modes have different As incorporation efficiencies. The other two remaining sectioned nanowires (D1 and E1 in figure S13) display a distorted hexagonal cross-section and uneven As incorporation, but seem to not conform to the core–shell scheme.

We verify the presence of As impurities along the length of nanowires grown with the AsH₃ preconditioning by performing atom-probe-tomography (APT) on a bent nanowire (see figure S14). Results show that As is present along the entire length of the nanowire, with an average As fraction in the probed volume ranging from $x = 0.06$ near the base to 0.04 towards the tip. These values are consistent with the As molar fractions measured by STEM-EDX (see figure S14). The slight decrease of As fraction along the nanowire could be due to the consumption of residual As overtime from the growth chamber.

A bending model is devised to gain a deeper understanding of nanowire bending and explain the results of the statistical analysis. The STEM-EDX cross-sectional study provides the basis for our model and serves as a source of input parameters (see figure S13). The APT study is not considered as a source of input parameters because the probed volume of the nanowire with this technique is too narrow to image the distribution of atomic species in the entire nanowire cross-sectional plane (see figure S14). Therefore, only the STEM-EDX cross-sectional study is used for constructing the bending

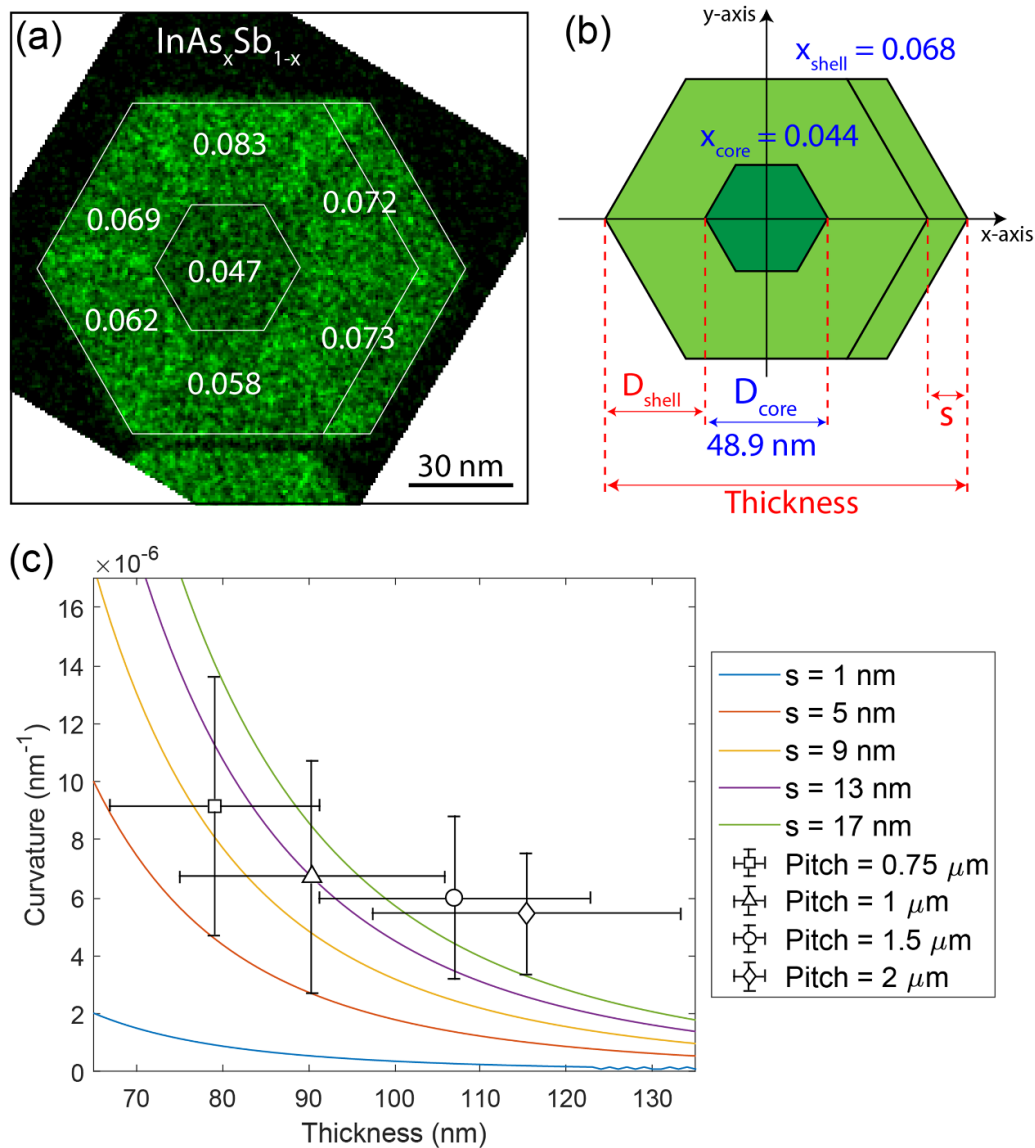


Figure 3. A representative As map from a STEM-EDX cross-sectional sample of a stemless InSb nanowire grown on InP using AsH_3 during heat-up. The quantified As molar fractions (x) in different parts of the cross-section are displayed. The accuracy in percentage As for areas selected within the STEM-EDX mapping is approximately 0.5 at%. Thus, the As molar fraction has an error of ~ 0.008 . The nanowire has an As-poor core surrounded by an asymmetric As-rich shell, with varying As molar fractions, outlined by the white lines. (b) Sketch of the nanowire cross-section used for the bending model, displaying the As molar fraction distribution, and input parameters. The nanowire is modeled as an $\text{InAs}_x\text{Sb}_{1-x}$ alloy with a hexagonal asymmetric core-shell As distribution constant along the entire nanowire length. As molar fractions in the core and shell are considered homogeneous. The asymmetry in the hexagonal shape is modeled with a thickening s of the nanowire shell along two adjacent $\{110\}$ side facets. The input parameters of the model are the core thickness (D_{core}), the As core molar fraction (x_{core}), and the As shell molar fraction (x_{shell}). The nanowire thickness is defined as the sum of D_{core} , s , and twice the shell thickness (D_{shell}). (c) Comparison between the calculated nanowire curvatures as a function of thickness resulting from the bending model, for different values of s , and the results of the statistical analysis of figures 2(e) and (f).

model. The nanowire cross-section is modeled as an asymmetric core-shell structure, uniform along the entire length of the nanowire, and is depicted in figure 3(b). The core and asymmetric shell are both considered homogeneous and composed of an $\text{InAs}_x\text{Sb}_{1-x}$ alloy, with two different As fractions and lattice constants correlated by Vegard's law [46]. The shell thickness D_{shell} is considered a variable, while the extent of the As-poor core area, given by the parameter D_{core} , is considered constant and equal to the average diameter measured among the sectioned nanowires. The asymmetry of the structure is

modeled by introducing a thickening, s , of two adjacent $\{110\}$ side facets, having the same As fraction of the shell. By iterating over a range of D_{shell} and s , the model calculates the nanowire curvature as a function of thickness resulting from the lattice mismatch. For further details on the bending model, see section SI.3 of the supplementary information.

Figure 3(c) displays the bending model results for several asymmetric thickening s and compares them to the statistical analysis results of figures 2(e) and (f). The range of s extends up to $s = 17$ nm, which is the largest observed

value in the STEM-EDX cross-sectional study. The experimental data and theoretical predictions show a similar trend and curvature order of magnitude, but deviations occur for thicker nanowires. These deviations can be attributed to the complex nature of the grown nanowires, which exhibit various distorted hexagonal shapes, variations in As fraction within the shell, and likely a variation in As incorporation along the nanowire. Additionally, the occurrence of the systematic error prior to SEM imaging, although causing minor changes to the curvature distributions and mean values, should be considered when comparing experimental and theoretical data. Overall, the bending model is in good agreement with the experimental results and captures the physics of nanowire bending in this system. The results of the model show that bending can be mitigated growing a thicker shell around the core, and this can be achieved by enhancing the VS InSb growth on the nanowire sidewalls by increasing the pitch.

The understanding of the stemless InSb nanowire growth using an AsH_3 heat up is applied to grow complex nanowire networks and nanoflakes on a InP(100) platform with inclined $\{111\}\text{B}$ facets, named trenches (see [25] for platform fabrication details). Dry etching is used to fabricate the nanoholes in the SiN_x mask. A schematic illustration of the platform for nanowire growth is shown in figure 4(a). Different InSb nanostructures can be grown in pre-determined positions and orientations by tuning the offset (Δy in figure 4(a)) of pairs of Au particles deposited on opposing facets of neighboring trenches. We adopt a large nanowire inter-distance along the trenches to favor the VS InSb growth on the nanowire sidewalls, which minimizes the bending and ensures that each nanowire interconnects with others with the intended Δy (figures 4(b) and S5). Stemless InSb nanowire and nanoflakes grown in this way display a thickness within the 120–180 nm range.

The stemless growth grants several benefits for the fabrication of complex semiconductor–superconductor device hybrids compared to stemmed nanowire growth. By tuning the growth time, stemless nanowires can interconnect with each other up to lengths of tens of microns, enabling the fabrication of larger nanowire networks ($>6.5 \mu\text{m}$, figure 4(c)) or nanoflakes ($>12 \mu\text{m}^2$, figure 4(d)) compared to stemmed ones that suffer from stem evaporation ($\sim 3.5 \mu\text{m}$ and $\sim 2 \mu\text{m}^2$, respectively) [24, 25]. The increased length broadens the design possibilities of superconductor–semiconductor hybrid devices, exploiting the smart inter-shadowing effect between neighboring nanostructures during the directional superconductor deposition. This selective superconductor deposition mitigates the need for potentially detrimental post-fabrication steps that could affect the quality of the devices, ensuring smooth and pristine interfaces [64–67]. Moreover, stem omission ensures that all the interconnecting nanowires are epitaxially connected to the substrate and thus merge without forming grain boundaries, as indicated by the trapezoidal morphology of all the final nanoflakes or nanobridges (see figures 4(b), (d) and

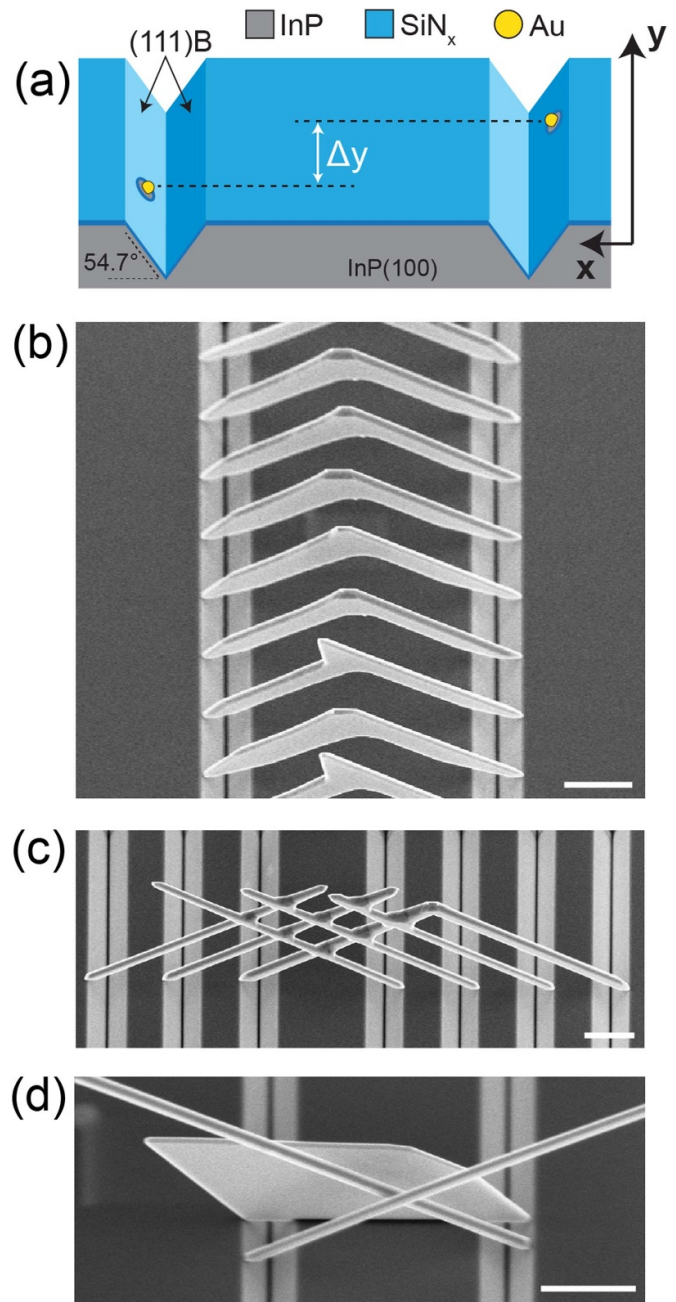


Figure 4. (a) Sketch of the platform for creating a wide variety of designs of InSb nanowire networks or nanoflakes. Control over the nanowire direction is achieved by patterning inclined InP $\{111\}\text{B}$ facets ('trenches') with Au droplets at specific locations on the substrate. Nanowire interconnection can be controlled by adjusting the offset (Δy) of pairs of Au droplets deposited on opposing facets of neighboring trenches, allowing for the fabrication of nanoflakes ($\Delta y = 0$), interconnecting nanowires ($\Delta y \sim t/2$, where t is the nanowire thickness), and standing nanowires ($\Delta y > t$). (b)–(d) Representative side-view SEM images of stemless InSb nanostructures grown on the platform. (b) A series of intentionally grown InSb nanoflakes in the early stages of growth, highlighting a high yield of perfectly merged nanowires. (c) An example of a large hashtag nanowire network, and (d) a nanoflake. Scale bars are all $1 \mu\text{m}$.

S5), a hallmark of the absence of grain or twin boundaries [25]. All the nanoflakes analyzed by TEM are single-crystalline.

4. Conclusion and outlook

In summary, we achieved the growth of stemless InSb nanowires on InP by patterning the substrate with Au droplets, restricting them with a selective-area mask and exposing them to AsH₃ before InSb growth. The confinement of the Au droplets within narrow nanohole cavities of the mask is crucial to ensure a high yield of vertical nanowire growth. We demonstrate that the use of AsH₃ prior InSb nanowire growth contaminates our growth chamber, resulting in a continuous incorporation of a small percentage of As impurities throughout the 90 min of the InSb growth ('As carry-over'). As impurities are unevenly incorporated in the VLS-grown core and the VS-grown shell of the nanowires. The variations in shell thickness and As content lead to an asymmetric InAs_xSb_{1-x} core-shell system with varying lattice parameter within the core/shell cross-sectional plane, leading to nanowire bending. We grow single-crystalline, long, stemless InSb nanowire networks and nanoflakes on an InP substrate with inclined {111}B facets by adopting growth conditions that enhance the VS growth on the nanowire sidewalls, yielding thicker and straighter nanowires. This method surpasses previous growth schemes based on stems for the fabrication of semiconductor-superconductor device hybrids through shadowing superconductor deposition, broadening the potential of this technique.

However, this work also highlights the drawbacks of using an As species to help nucleating InSb nanowires, as disorder is induced in the nanowires due to the As carry-over. A workaround could be to use short, As-contaminated InSb nanowires as stems for a following InSb growth performed in another As-free reactor or using molecular beam epitaxy (MBE), although the feasibility of this technique needs to be tested. Full MBE growth of stemless InSb nanowires could also be explored, using an As Cracker as the As source to initiate the InSb nanowire growth, and subsequently closing the As shutter to continue the growth of pure InSb nanowires from indium and antimony elemental sources. Nonetheless, it might still be challenging to completely eliminate the presence of As carry-over with this method, as reported in a prior study [45]. Alternatively, developing InSb platforms with inclined {111}B facets such as trenches or ridges, similar to those developed on InP [19, 25, 68] or InAs [21, 65], could eliminate the need for As or stems to initiate InSb nanowire nucleation [24].

Data availability statement

The data that support the findings of this study are openly available at the following URL/DOI: [10.5281/zenodo.10868471](https://doi.org/10.5281/zenodo.10868471).




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Author contributions

M R and T A J S contributed equally to this work. T A J S carried out the substrate fabrication and nanowire growth, M R and G B supported. P L developed the SEM image-recognition software ('APIMs') utilized for the statistical analysis of nanowire bending. M R, T A J S and G B performed the statistical analysis of nanowire bending, P L supported. M R and T A J S transferred nanowires for T E M J J and W H J P made FIB cuts for the TEM analysis. M A V performed the TEM analysis. S K performed the atom-probe tomography study under the supervision of O M, T A J S developed the bending model. M R carried out the comparison between statistical analysis, TEM data and bending model prediction. M A V and E P A M B provided key suggestions and discussions and supervised the project. M R wrote the manuscript with input from all the co-authors.

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